



Milan B. Radulović was born in Nikšić, Montenegro. He received his B.Sc. (study program: Electronics) and M.Sc. (study program: Robotics and Artificial Intelligence) in Electrical Engineering from the University of Montenegro, Montenegro, in 1992 and 1996, respectively. He received his Ph.D. (research field: Computer Architecture) in Electrical Engineering from the University of Belgrade, Serbia, in 2015.

He was previously with the University of Montenegro, for over a decade where he was involved both in teaching process and many research and development projects (robotics and artificial intelligence, electronics, telecommunications, computer networks, software development, database, and real-time control systems) at the Faculty of Electrical Engineering and University Computer Centre. Also, he was with the University Mediterranean, Montenegro, as a part-time assistant at the Faculty of Information Technology and Faculty of Economics and Business.

He was with Crnogorski Telekom A.D. (member of Deutsche Telekom Group) and Ericsson AB Stockholm Montenegro, for almost two decades, working as RAN/CORE engineer and Voice CORE team leader, respectively. He was involved in operation and maintenance, and development of radio and switching systems for the fixed and mobile network for two national operators, Crnogorski Telekom and m:tel.

He was included in several research projects as Visiting Research Fellow at the Universities abroad such as University of Warwick (UK), University of Granada (Spain), University of Siena (Italy), and EPFL (Switzerland). His current research interests are mainly in electronics, telecommunications, computer architecture (especially chip multiprocessor systems with thread-level speculation support), coherence and speculation protocols, transactional memory, compilers, parallel programming, and controlling of real-time systems. In these areas, he published papers in international scientific journals, books, and proceedings of international and domestic conferences. He is the member of HiPEAC (European Network of Excellence on High Performance and Embedded Architecture and Compilation) group.

For the results achieved during his education, work career and scientific research activities, he received several awards from educational institutions, then, from the Municipality of Nikšić, and from the companies he worked with, as well as from domestic and international scientific research institutions and organizations.

On April 18, 2024, he was appointed President of the Council of the Agency for Electronic Communications and Postal Services.

He has active knowledge of English, Italian and Spanish, and passive from German and Greek languages.